



TFW

Attorney Docket No. YOR920030603US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s) Luk et al.
Docket No.: YOR920030603US1
Serial No.: 10/751,714
Filing Date: January 5, 2004
Group: 2826
Examiner: Johannes P. Mondt

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *Ken Mason* Date: June 13, 2005

Title: Amplifiers Using Gated Diodes

TRANSMITTAL LETTER

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith is a Response to Restriction Requirement relating to the above-identified patent application. There is no additional claim fee due in connection with the Response.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **IBM Corporation Deposit Account No. 50-0510** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,

Kevin M. Mason

Kevin M. Mason
Attorney for Applicant(s)
Reg. No. 36,597
Ryan, Mason & Lewis, LLP
1300 Post Road, Suite 205
Fairfield, CT 06824
(203) 255-6560

Dated: June 13, 2005



DFW

Attorney Docket No. YOR920030603US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

5 Applicant(s) Luk et al.
Docket No.: YOR920030603US1
Serial No.: 10/751,714
Filing Date: January 5, 2004
Group: 2826
10 Examiner: Johannes P. Mondt

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature:

John Manno

Date: June 13, 2005

Title: Amplifiers Using Gated Diodes

15

RESPONSE TO RESTRICTION REQUIREMENT

Mail Stop Amendment
Commissioner for Patents
20 P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

25 This paper is responsive to the Examiner's Office Action dated May 13, 2005.

The Examiner found that the claims of the instant application, which are directed to circuits for amplifying signals, describe two distinct inventions. The Examiner therefore required that the application be restricted to one of the following inventions under
30 35 U.S.C. 121:

I. Claims 1-37, drawn to a circuit for amplifying signals including a method of amplifying signals apparently based on the use of said circuit, classified in class 365, subclass 207+; and

35

II. Claims 38-42, drawn to a semiconductor device, classified in class 257, subclass 312.

Applicants respectfully assert that the restriction requirement is improper and
40 should be withdrawn, since each Group is generally related to circuits for amplifying signals, and it is believed that a complete search for each Group would require a search of most, if not all, of the individual classes and subclasses. Accordingly, Applicants submit that an examination of both Groups would not impose a serious burden on the Examiner. Where, as

here, "the search and examination of an entire application can be made without serious burden, the Examiner must examine it on the merits, even though it includes claims to distinct or independent inventions." MPEP §803.

Accordingly, it is respectfully requested that the restriction requirement be reconsidered and withdrawn and that all of the pending claims in the application be examined together in this application. Alternatively, Applicants respectfully select Group I, claims 1-37, for prosecution on the merits and withdraw claims 38-42, corresponding to Group II, from prosecution.

Respectfully submitted,



Kevin M. Mason

Attorney for Applicant(s)

Reg. No. 36,597

Ryan, Mason & Lewis, LLP

1300 Post Road, Suite 205

Fairfield, CT 06824

(203) 255-6560

Dated: June 13, 2005